



**Series
T233-500**

**Phase Control Press-Pack
Thyristor
Type T233-500**

Center amplifying gate

Low on-state and switching losses

Designed for traction and industrial applications

Maximum mean on-state current	ITAV	500 A						
Maximum repetitive peak off-state and reverse voltage	UDRM	1000 ÷ 1800 V						
Turn-off time	tq	160; 200; 250 µs						
UDRM, URRM, V	1000	1100	1200	1300	1400	1500	1600	1800
Voltage code	10	11	12	13	14	15	16	18
Tvj, °C					- 60 ÷ 125			

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	T233-500	Conditions
ITAV	Mean on-state current	A	500 765	Tc=87 °C, Tc=55 °C, 180° half-sine wave, 50 Hz
ITRMS	RMS on-state current	A	785	Tc=87 °C
ITSM	Surge on-state current	kA	9,0 10,0	Tvj=125°C Tvj=25°C
I ² t	Limiting load integral	kA ² s	405 500	Tvj=125°C Tvj=25°C
UDRM, URRM	Repetitive peak off-state and reverse voltage	V	1000÷1800	Tj min≤Tvj≤TjM 180° half-sine wave, 50 Hz Gate open
UDSM, URSM	Non-repetitive peak off-state and reverse voltage	V	1100÷1900	Tj min≤Tvj≤TjM 180° half-sine wave tp=10 ms, Single pulse Gate open
(di _T /dt) crit	Critical rate of rise of on-state current : non - repetitive repetitive	A/µs	320 160	Tvj=125°C ; UD=0,67 UDRM, Gate pulse : 10V, 5 Ω, 1µs rise time, 10 µs
URGM	Peak reverse gate voltage	V	5	Tj min≤Tvj≤TjM
Tstg	Storage temperature	°C	-60÷80	
Tvj	Junction temperature	°C	-60÷125	

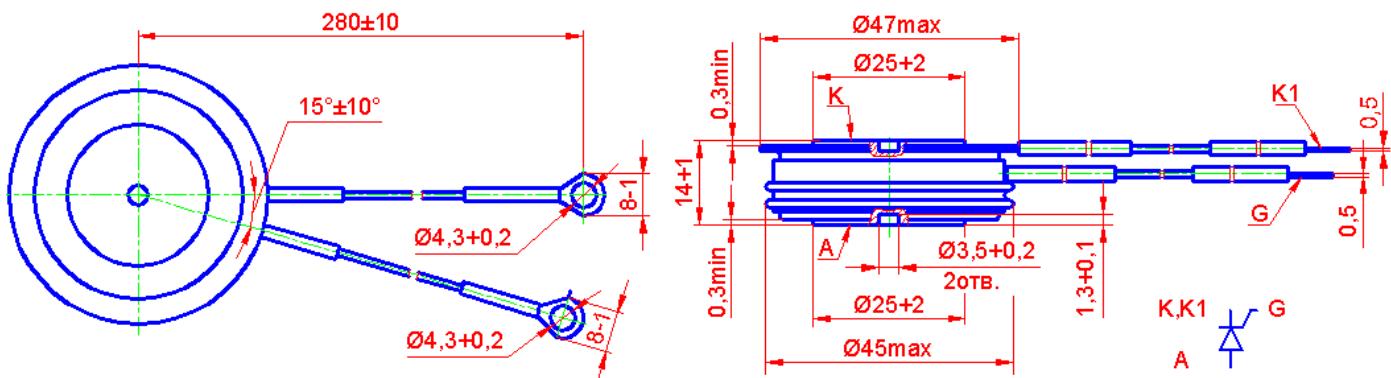
CHARACTERISTICS

UTM	Peak on-state voltage	V	1,8	Tvj=25°C, ITM=3,14 ITAV
UT(TO)	Threshold voltage	V	1,0	Tvj=125°C
RT	On-state slope resistance	mΩ	0,55	1,57 ITAV< IT <4,71 ITAV
IDRM IRRM	Repetitive peak off-state and reverse current	mA	50 50	Tvj=125°C, UD = UDRM UR= URRM

CHARACTERISTICS				
Symbols and parameters		Units	T233-500	Conditions
I _L	Latching current	A	0,7	Tvj=25°C, UD=12V Gate pulse : 10V, 5Ω, 1 µs rise time, 10µs
I _H	Holding current	A	0,5	Tvj=25°C, UD=12V, Gate open
UGT	Gate trigger direct voltage	V	2,5 5,0	Tvj=25°C, Tvj=-60°C UD=12V
IGT	Gate trigger direct current	A	0,3 0,85	Tvj=25°C, Tvj=-60°C
UGD	Gate non-trigger direct voltage	V	0,25	Tvj=125°C, UD = 0,67 UDRM Direct gate current
IGD	Gate non-trigger direct current	mA	10	
t _{gd}	Delay time	µs	1,6	Tvj=25°C, UD=500V ITM = 500 A
t _{gt}	Turn-on time	µs	3,2	Gate pulse : 10V, 5Ω, 1 µs rise time, 10µs
t _q	Turn-off time	µs	160÷250	Tvj=125°C, ITM = 500 A di _R /dt = 10 A/µs, UR=100V UD = 0,67 UDRM du _D /dt=50 V/µs
Qrr	Recovered charge	µC	1200	Tvj=125°C, ITM = 500 A
trr	Reverse recovery time	µs	20	
Irrm	Peak reverse recovery current	A	120	dir/dt = 10 A/µs, UR=100V
(dud/dt)crit	Critical rate of rise of off-state voltage	V/µs	500 1000	Tvj=125°C, UD = 0,67 UDRM Gate open
Rthjc	Thermal resistance junction to case	°C/W	0,045	Direct current, double side cooled

ORDERING							
	T	233	500	16	7	3	
	1	2	3	4	5	6	

1. Phase control thyristor.
2. Design version.
3. Mean on-state current, A.
4. Voltage code (16=1600 V).
5. Critical rate of rise of off-state voltage ($6 \geq 500 \text{ V}/\mu\text{s}$, $7 \geq 1000 \text{ V}/\mu\text{s}$).
6. Group of turn-off time ($\text{du}_D/\text{dt}=50 \text{ V}/\mu\text{s}$, $2 \leq 250 \mu\text{s}$, $P_2 \leq 200 \mu\text{s}$, $3 \leq 160 \mu\text{s}$).



Mounting force : 9 ÷ 12 kN
Weight : 120 grams